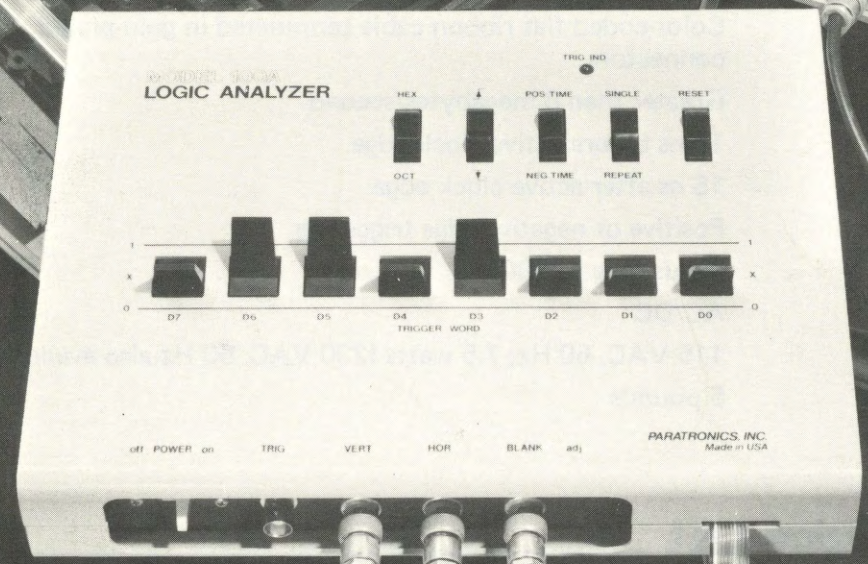
MODEL 100A
LOGIC ANALYZER

Features:

- ☐ LOW COST; USE WITH VIRTUALLY ANY SCOPE OR XY DISPLAY
- ☐ 128-BIT TRUTH TABLE (8 BITS WIDE BY 16 WORDS DEEP)
- ☐ "POSITIVE TIME" AND "NEGATIVE TIME" DISPLAY MODES
- ☐ STATIC OR DYNAMIC DISPLAY OF TRUTH TABLES
- ☐ HEXADECIMAL AND OCTAL DISPLAY FORMATS
- ☐ FULL COMPATIBILITY WITH POPULAR LOGIC FAMILIES



PARATRONICS, INC.

$229.00 kit
$295.00 assembled

The Model 100A Logic Analyzer converts an ordinary oscilloscope into one of the most useful digital testing, analysis, and development tools available. It is ideal for applications requiring an in-depth exploration of hardware and software performance. Use it to trace computer program flow, examine the contents of ROMs and other memories, check counter and register operations, and monitor I/O sequences. An input data rate in excess of 8 Megabytes/second assures compatibility with most microprocessors and general purpose digital systems.

The Model 100A provides data domain analysis features not possible using an oscilloscope alone. It will capture data before or after a trigger word, and display the associated truth tables statically or dynamically in your choice of hexadecimal or octal formats.

The low cost of the Model 100A permits engineering companies, educational institutions, and hobbyists to take advantage of data domain analysis to support a variety of advanced development, training, and troubleshooting applications.

The Model 100A is easy to operate and will interface directly with most common logic families including TTL, Schottky, CMOS, MOS, and DTL. Connection to the system-under-test is accomplished using a color-coded flat ribbon cable terminated in gold-plated "universal" pin connectors.

A 100-page owner's manual provides extensive information on theory of operation as well as educational, production testing, field service, and general digital applications. The analysis procedures for seven popular microprocessors are also included.

MODEL 100A Specifications

Display Type:	Data domain truth table
Display Format:	Hexadecimal and octal
Display Modes:	Single and Repetitive (i.e., static and dynamic truth table presentations)
Data Collection Modes:	Positive and negative time
Trigger Word:	8 bits wide
Trigger Swtiches:	3-position: "1," "0," "X" (don't care)
Trigger Indicators:	LED on front panel, also trigger word appears intensified in truth table
Trigger Output:	Auxiliary pulse which occurs when valid trigger word is detected (TTL level)
Trigger Bus:	Available at probe input (open collector TTL level)
Display Size:	1 byte (8 bits) X 16 words deep
Input Load:	360 μA (logic 0), 100 μA (logic 1)
Input Threshold:	TTL (approximately 1.6V) with hysteresis
Input Logic Levels:	Logic 0: –0.5V to 1.6V Logic 1: +1.6V to 15V } TTL, Schottky, DTL, CMOS, and MOS
Input Probes:	Color-coded flat ribbon cable terminated in gold-plated, "universal" pin connectors
Data Collection Rate:	Greater than 8 megabytes/second
Data Set-Up Time:	10 ns before active clock edge
Data Hold Time:	15 ns after active clock edge
Input Clock:	Positive or negative edge triggering
Blanking Output:	Adjustable to +30V
Oscilloscope Coupling:	AC/DC
Power:	115 VAC, 60 Hz; 7.5 watts (230 VAC, 50 Hz also available)
Weight:	5 pounds

MODEL 100A Price List

Assembled and Tested *1 Year Full Warranty*	$295.00	*(Both items include comprehensive manual entitled "Introduction to Logic Analyzers.")*
Kit *(One evening assembly)* *3-Month Parts Warranty*	$229.00	
"Introduction to Logic Analyzers"	**$ 4.95**	*(Price of manual may be applied towards purchase of Model 100A.)*

Prices subject to change without notice.

Ordering Information

SHIPPING & HANDLING:

Subject to local stocking representatives shipping & handling charge.

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